



# FLASH

Electronics & Telecommunication Engineering  
Departmental Newsletter

Volume 03, Issue 01, July- Dec 2018

## Inside this issue

### ➤ Departmental Details

- MoU Signed

### ➤ Student's Corner

- Inter College Technical fest
- Workshops/Seminars/In-house Trainings
- Antenna Group Initiative
- Expert Talks
- Field Visits
- Achievements & Felicitation

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### ➤ Faculty Initiatives

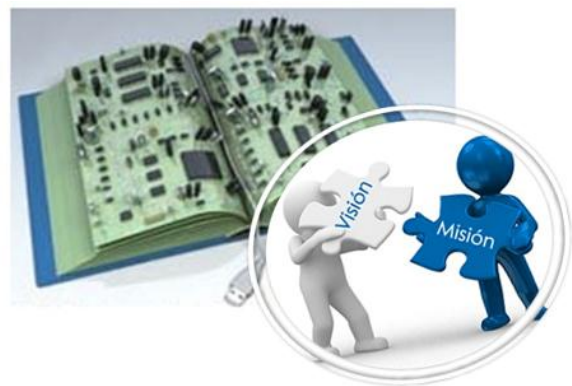
- TEQIP III AICTE-ISTE STTP
- Faculty Development Programme
- Certifications/ Awards
- Paper/ Journal Publications
- Workshops / Seminars / Expert Talks Conducted
- Workshops/ Seminars / Expert Talks Attended
- Community Outreach

### ➤ Training & Placement

### ➤ Upcoming Events

## VISION

To evolve into a Holistic Learning Hub that moulds technologically proficient engineers in the field of Electronics and Telecommunication; enhancing the global industry and society with Integrity, Ethics and Professionalism as envisaged by Don Bosco.



## MISSION

- To impart education abreast with the advances in technology and transform students into globally accepted professionals.
- To foster networking with all stakeholders for promoting technical innovation, research and entrepreneurship.
- To encourage extra-curricular activities and instil high levels of work ethics and responsibility for a better society.

# DEPARTMENTAL DETAILS

The Department has well qualified and experienced faculty in the areas of Electronics & Communication, VLSI, Embedded, Signal Processing, Automation and Control Engineering. The Department offers Bachelors of Electronics & Telecommunication Engineering programme, with total number of 60 seats in the 1st year and 12 seats for lateral entry into the second year.

The department has been organizing various programs to enhance the Industry -Institute interaction through field visit and workshops. Every year students of Third year and Final Year are exposed to Industry through internship programme of 1 month duration for industry experience.



Department has 10 fully functional Laboratories as listed below, along with the different Softwares.

## ➤ LABORATORIES

- Electronics Engineering
- Power Electronics and Control Systems
- Devices and Circuits
- Communication Engineering
- Microprocessor
- Electrical Engineering
- Electrical Technology
- Digital Systems and Electronic Instrumentation
- Signal Processing Lab
- Advanced Communication Lab
- Embedded Lab

## SOFTWARES

MATLAB  
Xilinx 14 Version  
OSCAD  
MAGIC  
LabVIEW

## KITS

8051 Kits  
ARM Kits  
PIC kits  
PLC kit  
LAN Trainer Kit  
OFC Kits

Dr. Varsha Turkar is the new Professor joined in the ETC department, having expertise in Digital image processing, Microwave Remote Sensing &, Machine Learning.



# Memorandum of Understanding (MoU)

## MoU with Autotech Builds Automation, Mapusa, Goa

A bipartite agreement in the form MoU was signed between Don Bosco College of Engineering, Fatorda, Goa and Autotech Builds Automation, Mapusa, Goa on 5<sup>th</sup> July 2018 in the Don Bosco Engineering College (DBCE) Campus.

Rev. Fr. Kinley D'Cruz (Director, DBCE) and Mr. Subodh Mone (Autotech, Mapusa, Goa) signed the MoU in the presence of Dr. Neena Panandikar (Principal, DBCE), Prof. Michelle Araujo (HOD, DBCE) and Prof. Flavia Leitao (Asst. Professor, DBCE).



After signing the MoU, Mr. Subodh Mone discussed the scope and objectives of the MoU, by stating that the MoU will:

- Provide Training and Internship as well as Placements at their organization or Placement Assistance at other organizations to the students of Don Bosco Engineering College.
- Conduct workshops, Training sessions, Guest lecturers and Technical model competitions at either organization as per the need and availability of the required resources.
- Conduct Field Visits at their organization or other organizations that are in collaboration with Autotech, Mapusa.



## MoU with Autotech Builds Automation, Mapusa, Goa

Don Bosco College of Engineering, Fatorda, Goa has signed Memorandum of Understanding (MoU) with Eduvance, the educational arm of Vanmat Technologies Pvt. Ltd., Santacruz (East), Mumbai on 28<sup>th</sup> Sept. 2018.

Rev. Fr. Kinley D'Cruz (Director, DBCE) and Dr. Johnathan Joshi (CEO-Eduvance) signed the MoU in the presence of Dr. Neena Panandikar (Principal, DBCE), Dilip Balasubramanian (Training manager- Eduvance), Asst. Prof. Trima P. Fernandes e Fizarado (In-charge HOD, DBCE), Prof. Varsha Turkar and Asst. Prof. Flavia Leitao.



After signing the MoU, Mr. Dilip Subramanian discussed the scope and objectives of the MoU, by stating that the MoU will:



- Set up an “Embedded Systems Lab” at the Electronics and Telecommunication Engineering Department with hardware (PSoC-4 ARM Cortex kits) and licensed PSoC creator software donated by Cypress Semiconductors.
- Offers value added courses in collaboration with Cypress University Alliance (CUA) and courses created by Eduvance.

Prof. Flavia Leitao is identified as the SPOC (Single Point of Contact) for all Eduvance related activities.

# STUDENT'S CORNER

## Inter College Technical fest – “TECHNOVA” & 7th installation of D-BEATS

The student association of the Department of Electronics and Telecommunication Engineering, Don Bosco College of Engineering, Fatorda, organized a two-day state level inter-college technical festival, TECHNOVA 2018 on the 12th & 13th of Oct. 2018.

The event was inaugurated by Mr. Sameer Neugui, Associate Consultant at TCS. The inaugural ceremony also included the installation of the new D-BEATS student council for 2018-19.



Distinction and first class students of the final year batch 2017-18 and former DBEATS council members were also felicitated.

The various events that were held on these days are as follows: Technical – IoT Masterchef, Research it Out, Circuit Masters, Code It, Line Follower, Robo Sumo, Robo Soccer and Non-technical-Brain Jammers, Snakes and Ladders, Counter Strike Go, Mock it, Rock it, Mock YouTube, Fifa 18, Ex-Quiz-Me, Restricted Battlefield, Kick Off, Shutter Up, Born to Wander, PUBG and Carrom.

Also talks on “Neural Network Security” and “Financial assistance and independence” were delivered.

Shutter up was a photography competition which had around 20 entries. The participants were given a theme on spot which was “leading lines”.



Research it Out was to give opportunity to present best innovative idea. Ideas such as use of waste vegetable oil for fuel, library management system, RADAR were presented.

IOT MasterChef was one of Technical event held in Technova2018, which was the great opportunity for the students to showcase their skills. Students from different college and higher secondary school from all over Goa had come to participate. IoT Masterchef consist total of 3 rounds. 1st round was quiz and in 2nd round students had to design a temperature sensor system and the 3rd round was to implement the circuit model of system on the breadboard.



IOT MasterChef was one of Technical event held in Technova2018, which was the great opportunity for the students to showcase their skills. Students from different college and higher secondary school from all over Goa had come to participate. IoT Masterchef consist total of 3 rounds. 1st round was quiz and in 2nd round students had to design a temperature sensor system and the 3rd round was to implement the circuit model of system on the breadboard.

The valedictory function was held on 13th October 2018. The Chief Guest for the function was Mr. Rohin Parker, Co-founder and CEO of MRINQ.

The event was successful enough to attract more than 1000 students for the two day fest from GEC, PCCE, SRIEIT, NIT GOA, AITD, BITS PILANI-GOA campus, Agnel Polytechnic, Goa University etc. The fest executed a total of 20 events and organized at its best of quality.

The main sponsor for the event was Nave Marg.



The winners of TECHNOVA 2018 were Goa College of Engineering, Farmagudi.

The coordinators for the event were Prof. Deron Rodrigues and Mr. Shreyas Aigal.

# WORKSHOPS/SEMINARS/IN-HOUSE TRAININGS

## Two Weeks Internship Programme for TE ETC

As a part of the In-house training for third year Electronics and Telecommunication Engineering students, the ETC Department of Don Bosco College of Engineering, in association with D-BEATS organized a ‘Two week internship programme from 2<sup>nd</sup> July to 13<sup>th</sup> July, 2018.

The sessions for the first week (2<sup>nd</sup> to 6<sup>th</sup> July 2018) were delivered by Mr. Zain Rajani, ALUMNI of ETC Department, Don Bosco College of Engineering.

First days’ sessions began with an introductory and interactive talk on “Introduction to VLSI Manufacturing”, followed by sessions on Modeling Using Verilog.

On the second day the following contents were covered: Blocking and Non-Blocking Statements, User Defined Primitives, Modeling Finite State Machines (FSM) and Switch Level Modeling.



Sessions on the third day involved writing codes and Test Benches for various Digital Circuits such as adders, subtractors, multiplexers, demultiplexers etc.

The fourth day began with session on “Introduction to R Programming” in which the resource person explained the need of R and its usage.

Fifth day session included the following topics: Loops and Data Management in R, Operation on Strings, Importing and Exporting Data in R, Dealing with Data Frames, Statistics in R and Graphics in R.

During the second week, the sessions were delivered by Mr. Rajaram Parsekar, Entrepreneur, Elementronix, who provided hands on experience to students on Raspberry Pi and Arduino.

The two weeks training was co-ordinated by Prof. Melba D’Souza and Prof. Deron Rodrigues.



# ANTENNA GROUP INITIATIVE

## Workshop on “ANTENNA DESIGN”

Electronics & Telecommunication Engineering Department in association with Antenna Research Group organized a one day workshop on Antenna Design for the students of second year of Electronics and Telecommunication Engineering on 8/10/2018.

Group coordinator Prof. Mohini Naik started the session with basic concepts of antenna and its parameters, types of antennas, microstrip patch antenna etc. Students got the opportunity to learn something new beyond the syllabus. In the next session, students were allotted with the projects on antenna design.



## Seminar on “GATE Awareness”

GATE Awareness Program was held for students of Electronics & Telecommunication Engineering & Civil Engineering by Prof. Yeshudas Muttu on 31<sup>st</sup> July & 1<sup>st</sup> August 2018. The seminar was held to make students aware about the GATE & its importance in engineering field.

The session educated students on how to prepare for GATE exam, GATE exam pattern (Technical Aptitude, Engineering Mathematics & Branch Subjects), subject-wise bifurcation and analysis of previous year's GATE Paper, GATE statistics over the years and so on.

## Seminar on “IEEE Awareness”

IEEE Awareness Program was held for students of Electronics & Telecommunication Engineering & Computer Engineering by Prof. Yeshudas Muttu on 23<sup>rd</sup> & 24<sup>th</sup> August 2018.

The students were made aware about activities like Seminars, Hands-on Workshops, Industrial Field visits, expert lectures and technical events to be organized under the student's branch for students as well as the faculty members on and off campus that helps in skill development & lifelong learning.





# EXPERT TALKS

## Field Expert talk on “ARTIFICIAL NEURAL NETWORKS”

An expert talk on 'Security for Artificial Neural Networks' was organized as a part of the intercollegiate event hosted by the ETC department of Don Bosco College of Engineering, 'Technova 2018' event on the 13th of October at 11am at the 2nd floor auditorium.

The session was delivered by Mr. Raj Kunkolienkar, Executive Lead at Seeker Space, Panaji-Goa. The session started off with an introduction to neural networks, types and working of different neural network architectures and the need for neural networks in today's world. Topics covered included how neural networks work, how they can be hacked and what can be done to protect against it. The session concluded with a brief description of the gradient-masking method used to defend against adversarial attacks on neural networks.



## Expert talk on “Trends in VLSI”

An expert talk was conducted on “Trends in VLSI”, by Prof. Anil Raiker, on the 5<sup>th</sup> Nov 2018, for the TE ETC students.

The expert talk was organized as a gap analysis for the “VLSI design and technology” course offered for the TE ETC students by the faculty members Prof. D. S Vidhya and Prof. Melba D'souza.

Prof. Anil Raiker talked about the recent trends in VLSI Design, Classification of VLSI, VLSI manufacturing processes, Tools for design and Languages.



## FIELD VISITS

companies, the Electronics and Telecommunication Engineering Department, Don Bosco College of Engineering, Fatorda, organized an industrial visit for the students of Final Year, on 30<sup>th</sup> August 2018 to OPTEL VISION, Verna Industrial Estate.

OPTEL is also adapting its technologies to provide End-To-End Traceability Solution for other sectors such as Food and Beverage Industry, smart manufacturing and natural resources such as cocoa, coffee, cotton, palm oil, nuts and minerals, etc. to enhance supply chain visibility and Transparency.



### **Field trip to “BOSCH PACKAGING UNIT”**

In the afternoon session, on 30<sup>th</sup> August 2018, the final year students visited BOSCH, Verna, Goa, which intends to meet the increasing demand of India’s fast growing packaging market as well as international markets. Along with global expertise, Bosch Packaging Technology offers tailor-made packaging solutions for the Indian market. The students were explained the difference between the vertical form seal machines and horizontal form seal machines.



## **FIELD TRIP TO “SMARTLINK”**

A field trip was organized for the students of second year ETC students at SMARTLINK, Verna Industrial estate on 30th August 2018.

In the SMARTLINK, DIGISOL branch, the assembling process of a WLAN router was demonstrated using Machine based (SMT) surface mount technology.

In the SMARTLINK TELESMAART branch, the Fiber manufacturing process was demonstrated that involved Fiber spooling, Splicing, use of diamond cutter, isopropyl alcohol and epoxy, Monitoring various parameters (ferrule radius, fiber radius, planar fiber undercut and angle error), Testing the fiber cord, Packaging process (own barcode and serial number allotted to fiber) and Termination process.



## **FIELD TRIP TO “FM INSTRUMENTATION”**

In the afternoon session, on 30<sup>th</sup> August 2018, the second year students visited FM Instrumentation, Ponda that undertakes turnkey jobs which include consultancy services, design, selection and sizing of control valves, design of control loops and working out of specifications, erection jobs commissioning and maintenance of instruments. They also have a well-equipped NABL Accredited Calibration and maintenance of instruments.



They have a setup for calibrating various devices & electro-mechanical workshop that undertakes work like X – ray machine assembly as well as PCB assembly. Visit to these industries allowed them to observe the various processes involved in manufacturing a router as well as an X – ray machine, related components & Calibration of devices.

## **FIELD TRIP TO “SIEMENS”**

A field trip was organized for the students of third year ETC students at SIEMENS, Verna Industrial estate on 13<sup>th</sup> July 2018.

At Siemens, students were explained and shown in batches the process of how PCBs are made for various devices and how products are manufactured.

The whole process begins by applying the soldering paste on the PCB which is then followed by the an inspection of the applied paste. The PCB is then mounted with the Surface Mount Devices (SMD) such as resistors, power capacitors, transistors and so on by a machine which is programmed to do the needful. Next the PCB passes through the Reflow Oven. The PCB at this stage is subjected to heat which melts the soldering paste and hence the mounted devices are now soldered to the board. After this process the PCB is again inspected by the AOI process (Automated Optical Inspection).

After this inspection other components such as Relays, jumpers, transformers and so on are manually placed on the board. The PCB now goes through a process called as Wave soldering; during this the manually mounted devices get soldered to the PCB.



## **FIELD TRIP TO “SIEMENS”**

On the same day, on 13<sup>th</sup> July 2018, the third year ETC students visited the NCAOR Centre.

Dr. M.V. Ramesh gave an introductory talk about the Antarctic continent and an insight into the research work carried out by the Indian scientists and the Research unit of India. Multiple video presentations were shown to the students to give them a proper and in depth idea of how flora and fauna survive on the icy continent. Dr. M.V. Ramesh also discussed various Electronics equipment's that are used to collect data.



## Student's Achievements & Felicitation

- Mr. Raj R Kubal successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 52%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Ms. Simran Vernekar successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 57%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Ms. Francy Cabral successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 49%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Mr. B. Pavan Kalyan successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 40%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Mr. Suyog Borker successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 46%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Ms. Valini D'Silva successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 40%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Ms. Miti Gaunekar successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 58%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Mr. Siddhant Nilesh Kunde successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 45%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Mr. Joel Nicolas Fernandes successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 42%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Mr. Abhishek Dhumvad successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 58%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Mr. Rahul Kotru successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” with a consolidated score of 49%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Mr. Gajanan Sangodkar, Mr. Kingsley Fernandes, Ms. Lata pawar, Mr. Velton Angelis D'silva, Mr. Saurabh Prabhu, Ms. Juella Franny De Oliveira, Mr. Renvick Fernandes, Mr. Vaishabh Jalmi successfully Completed 12 week NPTEL Certification course on “Introduction to Programming in C” conducted by IIT Madras & SWAYAM, from July to October 2018.
- Ms Lasandra fernandes, Saif Sayed Ahmed successfully Completed 12 week NPTEL Certification course on “Control Systems”, conducted by IIT Madras & SWAYAM, from July to October 2018.

# FACULTY INITIATIVES

## TEQIP III AICTE-ISTE STTP ON “ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING”

A one week **TEQIP III AICTE-ISTE STTP on “Artificial Intelligence and Machine learning”** was organized by the Electronics and Telecommunication Engineering Department of Don Bosco College of Engineering in association with Department of Technology, Shivaji University from 2<sup>nd</sup> July to 6<sup>th</sup> July 2018.

The workshop commenced with inaugural ceremony in the distinguished presence of Dr. P.V Anandmohan, Technology advisor, CDAC Bangalore who was the Chief Guest and Dr. Jaydeep Bagi, Director Department of Technology, Shivaji University who was the Guest of Honor of the occasion. The other guests present on the occasion were, Director of Don Bosco College of Engineering, Rev.Fr.Kinley D’Cruz, Principal of DBCE, Dr.Neena Panadikar, HOD of ETC Dept. Prof. Michelle Viegas, Professor at KLE Dr.Uday Wali.



The first session on day 1 commenced with the key note address by Prof. Dr. P V Anand Mohan, Technology advisor, CDAC Bangalore, who gave a brief insight on Artificial Intelligence & Machine learning. The next session was by Prof Dr. Rashmi Rachh, Associate professor, VTU Belgavi who explained various models and algorithms associated with machine learning. Third session for the day was given by Prof. Dr. Kotrappa Sirbi, Professor, KLE DR MSS CET, Belgavi who gave in depth knowledge on data sciences.

This session was followed by hands on session on Python and anaconda programming by Ms. Shilpa Mayannavar, Research scholar at C-Quad, Belagavi



The second day of the workshop commenced with hands on session on “Python Programming” by Ms. Shilpa Mayannavar. The Post lunch session was delivered by Mr. Abhinandan Shirahatti, Assistant professor KLE DR MSS CET, Belgavi. He delivered a session on “Natural language processing and Information Retrieval.

On day 3, the session commenced with the introduction to the concept of texture analysis the session was delivered by Dr.Lingaraj Hadimani, Assistant Professor,KLE Belgavi. The second session was by Mrs.Geeta Navalyal, From KLE ,Belgavi which covered mobile healthcare system and benefits of machine learning in brain control interface.



On the fourth day the resource person for the day was Dr Satyadhyan Chickerur from the NVIDIA team. The fifth day of the workshop, commenced with a session by Dr. V. M. Aparanji , Assistant Professor, SIT Tumakuru on “Building a modular ANN library using R” in which she introduced the various operators and data types in R used for neural networks.

A total of 33 participants attended the workshop from different institutes. The coordinators for the workshop were Prof.Samantha Cardoso and Prof.D.S Vidhya.



## FACULTY DEVELOPMENT PROGRAMME (FDP) on “ARM CORTEX”

To provide knowledge sharing on the latest technologies with the faculty, the Department of Electronics and Telecommunication Engineering of Don Bosco College of Engineering (DBCE) organized a one Day Faculty Development Programme (FDP) on “ARM Cortex” on 29th Sept. 2018, as a part of the MoU signed between Eduvance and DBCE, where the two expressed mutual interest in setting up an “**Embedded Systems Lab**” at the Electronics and Telecommunication Engineering Department with hardware donated by Cypress Semiconductors.

The Embedded Systems lab setup included 7 PSoC-4 Bluetooth Low energy kits (ARM Cortex M0 Based) and PSoC Creator Software with user licenses.



The resource person for the FDP was Mr. Dilip Balasubramanian, Training Manager – Eduvance.

The FDP started with a brief introduction on Embedded Systems and System on Chip (SoC). Then PSoC creator software was introduced where the faculty was taught how to create a project, and the stages of design which included Design Schematic Entry, Design Drawing and main.c. The participants were then given a detailed understanding of all the components on the PSoC4 kit. After which they did a complete hands-on of three applications using the RGB LED, ADC and the PWM.

The coordinators for the FDP were Dr. Varsha Turkar and Asst. Prof. Flavia Leitao.



## CERTIFICATIONS/ AWARDS

- Prof. Deron Rodrigues successfully Completed 12 week NPTEL Certification course on “Control Systems” with a consolidated score of 88%, conducted by IIT Madras & SWAYAM, from July to October 2018.
- Prof. D. S Vidhya successfully Completed 12 week NPTEL Certification course on “Hardware modeling using Verilog” with a consolidated score of 72% conducted by IIT Madras & SWAYAM, from July to October 2018.
- Prof. Trima P. Fernandes e Fizardo successfully Completed 12 week NPTEL Certification course on “Digital Curcuits” with a consolidated score of 57 % conducted by IIT Madras & SWAYAM, from July to October 2018.
- Prof. Dr. Varsha Turkar received travel grant of Rs. 18,000 from **IEEE GRSS** to **attend IEEE WIE International Leadership Summit Kochi** on 7<sup>th</sup> & 8<sup>th</sup> September 2018

## PAPER/ JOURNAL PUBLICATIONS

- Dr. Varsha Turkar, research paper entitled "Classification of RISAT-1 Data using SVM with Hybrid Polarimetric Descriptors" has been presented at “International Conference on Innovations in Engineering, Technology and Sciences on 20 and 21 Sept. 2018 and will be published in IEEE explorer.
- Prof Mohini Naik, research paper entitled “Design and Simulation of Frequency Reconfigurable Micro strip Patch Antenna for C band and X band Applications” has been presented at “ IEEE International Conference On Computing, Communication, Control And Automation” organized by IEEE Pune section, published in IEEE XPLORE July 2018.

## WORKSHOPS/SEMINARS/EXPERT TALKS CONDUCTED

- Prof. Mohini Naik conducted a one day workshop on “**Antenna Design**” for the students of SE ETC on 8<sup>th</sup> Aug, 2018, as an initiative of the Antenna Research Group.
- Prof. Yeshudas Muttu conducted a GATE Awareness Program for the students of Electronics & Telecommunication Engineering & Civil Engineering on 31<sup>st</sup> July & 1<sup>st</sup> August 2018.
- Prof. Yeshudas Muttu conducted a IEEE Awareness Program for the students of Electronics & Telecommunication Engineering & Computer Engineering on the 23<sup>rd</sup> & 24<sup>th</sup> August 2018.

## WORKSHOPS/SEMINARS/EXPERT TALKS ATTENDED

- Prof. Michelle Araujo, Prof. Trima P. Fernandes e Fizardo, Prof. Kimberly Moraes, Prof. Samatha Cardozo, Prof. D. S Vidhya, Prof. Anisha Cotta, Prof. Melba D'Souza, Prof. Deron Rodrigues, Prof. Flavia Leitao, Prof. Mathilda Colaco, Prof. Yeshudas Muttu, Prof. Mohini N Naik, Prof. Vishnu Rathod, attended a one week TEQIP III AICTE ISTE STTP on "Artificial Intelligence and Machine Learning" organized by Department of Electronics and Telecommunication Engineering in association with Shivaji University, Kolhapur, from 2<sup>nd</sup> July 2018 to 6<sup>th</sup> July 2018.
- Prof. Michelle Araujo, Prof. Trima P. Fernandes e Fizardo, Prof. Kimberly Moraes, Prof. Samatha Cardozo, Prof. D. S Vidhya, Prof. Melba D'Souza, Prof. Deron Rodrigues, Prof. Flavia Leitao, Prof. Mathilda Colaco, Prof. Yeshudas Muttu, Prof. Mohini N Naik, Prof. Vishnu Rathod, attended a 1 day FDP on Smart Class, organized by the Training and Placement, DBCE, on 14<sup>th</sup> July 2018.
- Dr. Varsha Turkar, Prof. Trima P. Fernandes e Fizardo, Prof. Kimberly Moraes, Prof. Samatha Cardozo, Prof. D. S Vidhya, Prof. Melba D'Souza, Prof. Deron Rodrigues, Prof. Selvyn fernandes, Prof. Flavia Leitao, Prof. Mathilda Colaco, Prof. Yeshudas Muttu, Prof. Mohini N Naik, Prof. Vishnu Rathod, Prof. Shreyashri Desurkar, attended a 1 day FDP on ARM Cortex, organized by the ETC Department in association with Eduvance Technologies, on the 29th September 2018.
- Dr. Varsha Turkar, Prof. Trima P. Fernandes e Fizardo, Prof. Kimberly Moraes, Prof. D. S Vidhya, Prof. Melba D'Souza, Prof. Selvyn fernandes, Prof. Mathilda Colaco, Prof. Yeshudas Muttu, Prof. Mohini N Naik, Prof. Shreyashri Desurkar, attended a FDP on Technical Paper Writing, organized by the Training and Placement, DBCE, on 30th November 2018.
- Dr. Varsha Turkar, Prof. Michelle Araujo, Prof. Trima P. Fernandes e Fizardo, Prof. Kimberly Moraes, Prof. Melba D'Souza, Prof. Yeshudas Muttu, Prof. Mohini N Naik, Prof. Shreyashri Desurkar, attended a FDP on IPR and Patents, organized by the Training and Placement, DBCE, on 6th December 2018.
- Prof. Melba D'Souza and Prof. Mohini N. Naik, attended a 2-day National conference on Emerging Trends in computer and Electronics Engineering (NCETCE), organized by the Computer Engineering, DBCE, on the 8th to 9th November 2018.
- Prof. Michelle Araujo, Prof. Vidhya, Prof. Mathilda Colaco, Prof. Yeshudas Muttu, attended a FDP on NPTEL, organized by IIT Madras on July 21st 2018.

## COMMUNITY OUTREACH

### ➤ ATL (ATAL Tinkering Lab) Lab

An ATL (ATAL Tinkering Lab), is an initiative by the Government of India to foster curiosity, creativity and imagination in young minds; and inculcate skills such as design mindset, computational thinking, adaptive learning, Physical computing, etc.

As a part of the initiative, a workshop was conducted by Prof. Mohini Naik from Don Bosco College of Engineering, Fatorda on 'Satellites' on the 17th November 2018, at St Xavier's Higher Secondary School, Mapusa.



The mentor described how the rocket is launched along with the satellite and how the satellite is placed in the respective orbit; different orbits of earth are LEO, MEO, GEO, and HEO. She briefly explained about the orbits of mars and moon. She also mentioned about different types of satellites such as polar, geostationary etc, she demonstrated students about how to use the satellite tracking app.

Ms. Mohini Naik invited students present for the workshop to join in hands to take this topic to a practical level, where students can launch their own satellite. She shared few details about her project which she is undertaking with students from Don Bosco College of Engineering, Fatorda, where they are planning to design and launch a student satellite.

➤ **Unnat Bharat Abhiyan (UBA)**

Don Bosco College of Engineering (DBCE) has been selected as a participating Institute to carry out activities under Unnat Bharat Abhiyan (UBA).



The role of Institute is to make villagers aware about UBA Scheme that will have a great impact on their lives.

Prof. Yeshudas Muttu along with Prof. Sweta Morajkar visited NSS camp, Government High School. Prof. Yeshudas Muttu delivered a talk on “Skills for Learning, Skills for Study” that helped students to make & execute their own study time table and also to manage their extra-curricular activities.

➤ **Career Guidance**

Prof. Deron Rodrigues delivered a session titled “Career in Electronics and Telecommunication Engineering” at the Seminar “Career in Engineering” held for students of 10th, 11th and 12th Standard on the 28th of July 2018 at Don Bosco College of Engineering, Fatorda.

## TRAINING & PLACEMENT

The ETC Engineering Department maintains a vibrant industry-academia relationship and the suggestions of the experts are taken as tools for its continuous improvement. The third year and final year students undergo a 2 week Internship Program under the guidance of experience professionals. This is to bridge the gap between education and Industry.

A number of graduated students of the college are now occupying highly responsible positions in various reputed and prestigious organizations including Sankalp Semiconductors Pvt Ltd, Citrix, Visteon Technical & Services Centre Pvt. Ltd and Birla Furukawa Fibre Optics Pvt Ltd.

## UPCOMING EVENTS

- Two weeks Certified Course on Embedded Systems and IoT for the TE and BE students from 2<sup>nd</sup> Jan to 12th Jan 2019.
- Electromagnetics workshop scheduled for the Antenna Research Group on the 12th Jan 2019.
- E-Yantra Workshop (FDP) to be hosted for IITB on the 11th -12th Jan 2019.
- IEEE Student's branch Inaugural Function & Student Satellite Club Inaugural & SKEP (Skill & Knowledge Enhancement Programme) scheduled on the 1st & 2nd Feb, 2019.
- Field trip to ISRO Bangalore, as an initiative from IEEE, scheduled on the 9th Feb,2019.
- Field trip for the Electronics students to different Industries, schedules on the 28th Feb, 2019.
- MATLAB workshop for SE ETC students and BSS Technical paper presentation for the TE ETC students on the 9<sup>th</sup> March 2019.
- Expert lectures scheduled for the ETC students to get industry exposure.
- LaTeX Workshop for the IEEE Students and LabVIEW Workshop TE ETC students on 23rd Mar, 2019.